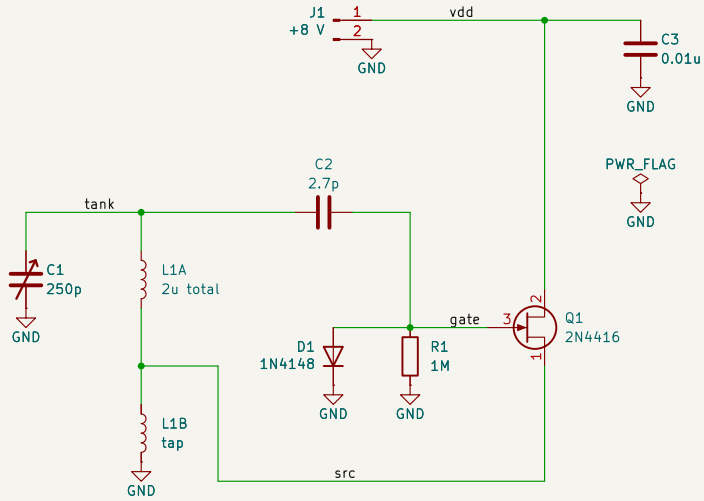


L1: 2 uH, tapped about 20% of the turns up from ground (EMRFD p. 4.3). C1: 250 pF tuning.

D1 (anode at gate) clamps the gate and sets the operating level.

Redrawn for QRPedia by NT7S. No output port in the figure; buffer from the source tap (see W7EL buf
L1 is one 2 uH coil tapped about 20% of the turns from ground (EMRFD text); drawn as L1A + L1B
Source: EMRFD (Hayward, Campbell, Larkin) Fig 4.4, p. 4.3. Errata: text-only (p. 4.3)

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Title: Hartley VFO with 2N4416 JFET

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